

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Philip S. Honsinger et al. Serial No.: 09/__,_

(RULE 1.53 (b) DIVISIONAL OF:

Serial No.: 09/015,819 filed on January 29, 1998) Group Art Unit: Unknown

Filed: Concurrently Herewith Examiner: Unknown

For: METHOD OF AUTOMATED DESIGN AND CHECKING FOR ESD ROBUSTNESS

Assistant Commissioner for Patents

Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 C.F.R. §§ 1.97 through 1.99 and pursuant to applicants' duty of disclosure under 37 C.F.R. § 1.56, applicants bring the documents, listed on PTO form 1449, to the attention of the Examiner in charge of the above identified patent application. Copies of the references can be found in the parent case (U.S. Patent Application Serial No. 09/015,819).

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the documents be considered by the Examiner and formally made of record in this application.

Respectfully submitted,

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